Miniaturised two-layer slit-patch structure for decoupling printed antennas

Li, Q.; Feresidis, A. P.

DOI: 10.1049/el.2013.1965

License: Other (please specify with Rights Statement)

Document Version
Peer reviewed version

Citation for published version (Harvard):

Link to publication on Research at Birmingham portal

Publisher Rights Statement:
(c) 2013 IEEE. Personal use of this material is permitted. Permission from IEEE must be obtained for all other users, including reprinting/republishing this material for advertising or promotional purposes, creating new collective works for resale or redistribution to servers or lists, or reuse of any copyrighted components of this work in other works.

Checked August 2015

General rights
Unless a licence is specified above, all rights (including copyright and moral rights) in this document are retained by the authors and/or the copyright holders. The express permission of the copyright holder must be obtained for any use of this material other than for purposes permitted by law.

• Users may freely distribute the URL that is used to identify this publication.
• Users may download and/or print one copy of the publication from the University of Birmingham research portal for the purpose of private study or non-commercial research.
• Users may use extracts from the document in line with the concept of 'fair dealing' under the Copyright, Designs and Patents Act 1988 (?)
• Users may not further distribute the material nor use it for the purposes of commercial gain.

Where a licence is displayed above, please note the terms and conditions of the licence govern your use of this document.

When citing, please reference the published version.

Take down policy
While the University of Birmingham exercises care and attention in making items available there are rare occasions when an item has been uploaded in error or has been deemed to be commercially or otherwise sensitive.

If you believe that this is the case for this document, please contact UBIRA@lists.bham.ac.uk providing details and we will remove access to the work immediately and investigate.
Miniaturised Two-layer Slit-Patch Structure for Decoupling Printed Antennas

Q. Li and A. P. Feresidis

A miniaturised double-layer slit-patch structure is introduced for decoupling two closely spaced CPW-fed printed monopoles with a common ground. The double-layer structure consists of a slit etched on the common ground between the antennas and a conducting dipole element printed on a very thin dielectric film separating the two layers. Simulated results of the antennas' performance as well as measurements of fabricated prototypes are presented.

Introduction: Mutual coupling effects between the individual elements of antenna arrays, such as those employed in modern compact multiple-input-multiple-output (MIMO) communication devices with stringent space restrictions, degrade the performance of the overall antenna system. The reduction of mutual coupling between closely spaced antennas has attracted significant research interest in recent years. Improved antenna designs that result in lower mutual coupling values have been presented [1]. The use of single [2] and multiple [3] defects (slots or slits) on a ground plane has also been proposed due to the ease of fabrication and applicability with different antenna types. Miniaturised convoluted slit geometries were recently presented for decoupling PIFAs on handheld devices [4].

In this paper, novel miniaturised two-layer slit-patch structures are proposed for significantly reducing the mutual coupling between two printed co-planar waveguide (CPW) fed monopoles. The proposed linear miniaturized structures not only allow for a very close separation between the monopoles but also occupy very little space on a very compact ground plane. The results of S-parameters, surface currents and radiation patterns are presented. Two fabricated prototypes have been experimentally tested and the measurements are presented.

Decoupling printed monopoles on compact ground plane: A two-element CPW-fed monopole array is printed on a 60x50mm², 1.5 mm thick, FR-4 dielectric substrate ($\varepsilon_r$=4.5). The layout and dimensions of the original two-element array configuration, designed to operate at a central frequency of approximately 2.45GHz is depicted in Fig. 1(a). At this frequency, the two printed monopoles are 14 mm apart, i.e. approximately 0.11 free-space wavelengths. A photograph of the fabricated structure is also shown in the same figure. Due to the small separation between the two antenna elements, the simulated and measured S21 between them was found to be just over -14 dB as shown in Fig. 2a. The mutual coupling between the two monopoles is mainly attributed to the strong currents that are excited on the common ground plane as shown in Fig. 5a.

A very thin two-layer linear slit-patch structure is proposed here as a miniaturized element inserted in the common ground plane between the two monopoles in order to significantly reduce their mutual coupling. A short linear slit (9.65mm length and 1mm width) is etched at the centre of the ground plane, as shown in Fig. 1(b). Due to the small length of the slit, which is much less than quarter-wavelength, its effect on the S21 is very small and is not shown here for brevity. A conducting patch (4mm length and 0.4mm width) is printed on a very thin (0.055 mm) dielectric substrate with $\varepsilon_r$=3 and is positioned centrally over the linear slit as shown in Fig. 1b. With the proposed slit-patch structure between the two printed monopoles, a minimum is obtained in the mutual coupling (S21) between the two antennas, due to the strong resonance of the slit-patch structure. An isolation better than -24dB in both simulation and measurement is achieved at the resonant frequency of the monopoles, as shown in Fig. 2(b). When the separation between two antennas reduces to 8mm, compared with a conventional ground plane, the insertion of the two slit-patch structures (shown in Fig 3) reduces the measured S21 from -12dB to values lower than -20dB across antenna operating frequency band, as shown in Fig 4. There is a slight discrepancy between measurements and simulations due to the fabrication inaccuracies as well as the manual alignment of the conducting patch over the slit.
Fig. 3 Two Printed monopoles (8mm) with two slit-patch structures

Fig. 4 Simulated and measured S-parameters of the printed monopole array with two slit-patch EBG structure shown in Fig. 3

Fig. 5 Current distribution
a Two-element array (with a uniform ground plane)
b A miniaturized slit-patch structure is inserted in the ground plane

Fig. 6 Total efficiency

Currents and radiation patterns: When the printed CPW monopole array is built with a compact uniform ground plane (Fig. 5a), a significant current propagates along the edge of the ground thus producing a high mutual coupling between the antenna elements. After the proposed slit-patch structure is inserted in the ground between the two antennas (Fig. 5b), it is evident that very little current reaches the passive element on the left. The total efficiency has also been calculated from the simulated results using: \( \eta_{\text{tot}} = \eta_{\text{rad}}(1 - |S_{21}|^2 - |S_{11}|^2) \). The total efficiency of the antenna array shows a slight improvement of just over 5% across the operating bandwidth after the slit-patch structure is employed between two antennas (Fig. 6).

Conclusion: A novel approach for realizing miniaturised structures that significantly reduce the electromagnetic coupling between closely spaced printed monopoles has been presented. An S21 minimum of less than -24dB was obtained by virtue of the resonance of a very thin two-layer slit-patch structure while the total antenna efficiency increased by approximately 5%.

Acknowledgment: The authors would like to thank CST for providing licenses for CST Microwave Studio™.

Q. Li (Electronics and Photonics, Institute of High Performance Computing, A*STAR, 138632, Singapore) E-mail: liq@ihpc.a-star.edu.sg

A. P. Feresidis (School of Electronic, Electrical and Computer Engineering, University of Birmingham, B15 2T, UK) E-mail: a.feresidis@bham.ac.uk

References